

Electronic Patent Application Fee Transmittal

Application Number:	10678166			
Filing Date:	06-Oct-2003			
Title of Invention:	METHOD FOR COMPRESSING SEMICONDUCTOR INTEGRATED CIRCUIT, USING DESIGN REGION DIVIDED INTO PLURAL BLOCKS			
First Named Inventor/Applicant Name:	Naoyuki Tamura			
Filer:	Marvin Jay Spivak/Tallat Mahmood			
Attorney Docket Number:	243650US2S			
Filed as Large Entity				
Utility Filing Fees				
Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Basic Filing:				
Pages:				
Claims:				
Miscellaneous-Filing:				
Petition:				
Patent-Appeals-and-Interference:				
Post-Allowance-and-Post-Issuance:				
Utility Appl issue fee	1501	1	1400	1400
Publ. Fee- early, voluntary, or normal	1504	1	300	300

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Extension-of-Time:				
Miscellaneous:				
Printed copy of patent - no color	8001	1	3	3
Total in USD (\$)				1703